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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10072165	02/07/2002	326	30	2819	D. Coughlin

**APPLICANTS: Coughlin Terry; Wang Geoffrey;

**CONTINUING DATA VERIFIED:

None

DC

** FOREIGN APPLICATIONS VERIFIED:

None

DC

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☒ no

35 USC 119 conditions met ☐ yes ☒ no

Verified and Acknowledged Examiners's initials *DC*

ATTORNEY DOCKET NO

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TITLE : ASIC architecture for active-compensation of a programmable impedance I/O

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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